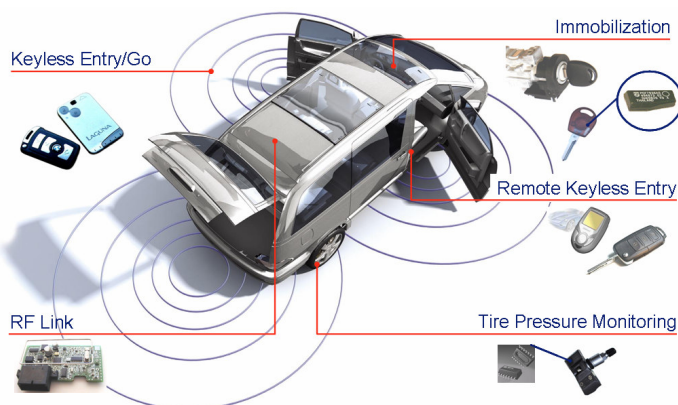


Masterthesis at NXP

NXP is a top 10 semiconductor company founded by Philips more than 50 years ago. Headquartered in Europe, the company has 37,000 employees working in 20 countries across the world. NXP creates semiconductors, system solutions and software that deliver better sensory experiences in mobile phones, personal media players, TVs, set-top boxes, identification applications, cars and a wide range of other electronic devices.



NXP Semiconductors provides world-wide leading chip solutions for the Identification market, including ICs for smart cards, smart labels / tags and car access and immobilizers. Building on its leadership position in contactless chip solutions and high-level security for smart cards, NXP supports a wide range of application areas - from banking and mobile communications to e-government, public transport, supply chain management and logistics.

Design of a VCO

For future automotive applications which are linked to wireless communications, specific transceiver concepts in the 900 MHz range have to be investigated. Therefore it is aim to develop and design IP blocks in CMOS Technology. Integrated voltage-controlled oscillators (VCOs) are usual utility blocks in modern communication systems and are typically used as local oscillators to upconvert or downconvert the radio frequency signal. Aim of the described masterthesis is it to design a VCO under the constraints of low power, low phase noise and in particular working for different frequency bands.

Details and Workplan:

- ✚ Literature study of different/possible concepts – comparison of concepts
- ✚ Parameter-Characterisation – constraints due to application (Power, Frequency, Noise,...)
- ✚ Choice of a specific topology
- ✚ Design the chosen topology in Cadence (under guidance of an experienced designer)
- ✚ Layout
- ✚ Performance characterisation by measurements
- ✚ Writing your thesis

General Parameters:

- ✚ Typical duration: 6 months
- ✚ Financial conditions according to NXP guidelines
- ✚ Contact person: Dr. Harald Witschnig (harald.witschnig@nxp.com)

Ansprechpersonen am Institut für Elektronik:
 Dr. P. Söser (x7539), Prof. W. Pribyl (x7520)